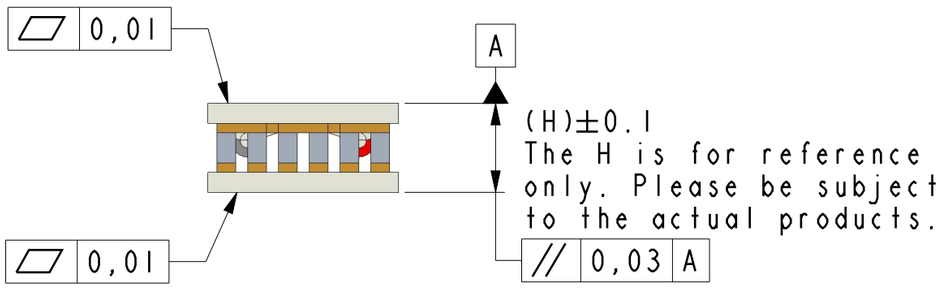
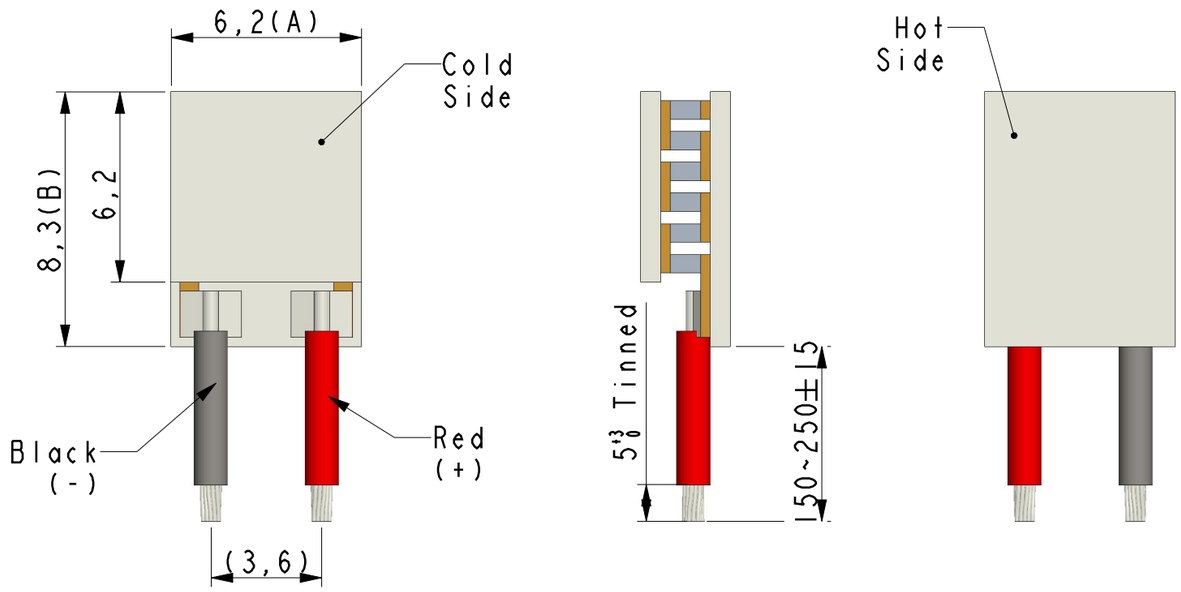
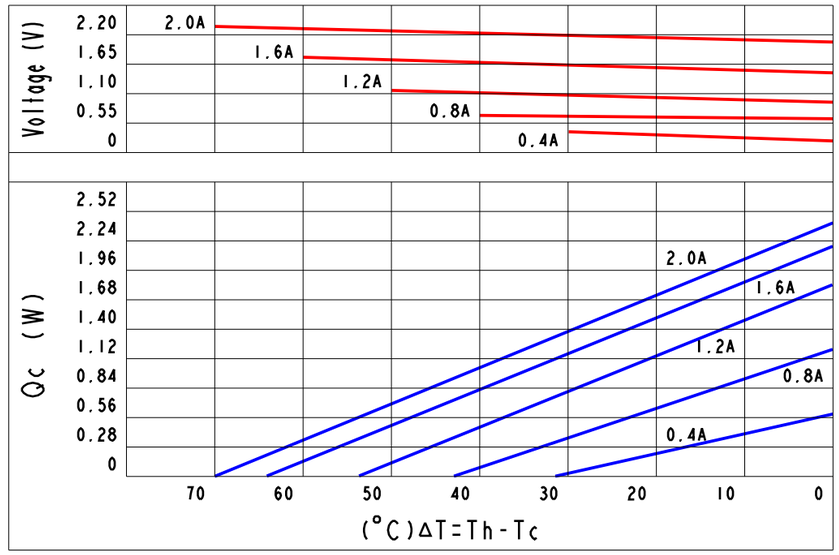
REVISIONS						
REV.	POS.	DESCRIPTION	DATE	DRW	APP	ECR#
A		INITIAL CREATION	2013/09/09	Gory	Moson	





Curve Chart(Be Confined To TESI-018206283):

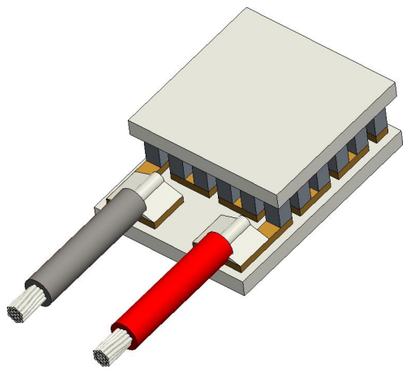


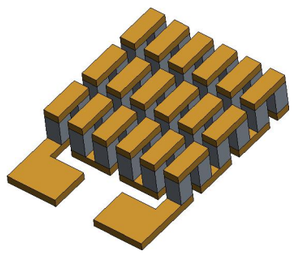
Part Number And Feature:

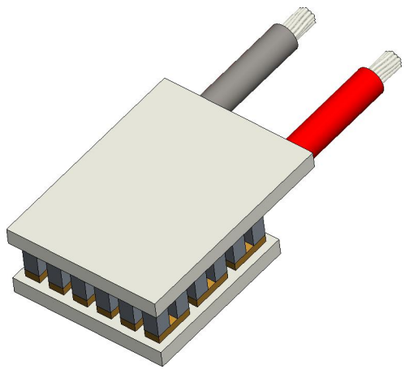
T E S I - 0 1 8 x x 6 2 8 3		Sealing	NO
↧ ↧ ↧ ↧ ↧		Operation Temperature	125°C(Max.)
Thermo Electric Chip(Small) / Stage Stack / N & P Stack Quantity / Current A(Max.) / Dimension (A) / Dimension (B)		Melting Point	138°C
		Storage Temperature	-60°C~100°C
		RoHS	YES

Technical Data:

ITEM	Part NO.	Stack(P&N)	A(Max,)	V(Max.)	Qc(W) /Th=27°C/ ΔT(°C)		DIM(A)	DIM(B)	DIM(H)
1	TESI-018126283	18	1.2 A	2.2 V	1.5W	70°C	6.2	8.3	RF2.9
2	TESI-018156283	18	1.5 A	2.2 V	1.9W	70°C	6.2	8.3	RF2.7
3	TESI-018206283	18	2.0 A	2.2 V	2.5W	70°C	6.2	8.3	RF2.3







Notes:
1. Printing always on cold side.
2. Torlerance of thermo and electric parameters ±10%.
3. Please mount heat sink before you use it. also, please do not exceed the extra voltage at any time.
4. Please contact with us if you need Melting Point 183°C (Operation Temperature 150°C Max.) and 235°C(Operation Temperature 200°C Max.) type.

*DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION

1. UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE MM
2 TOLERANCE ARE AS FOLLOWS:
0 < X < 2 ± 0.06
2 < X < 10 ± 0.08
10 < X < 50 ± 0.12
50 < X < 100 ± 0.16
100 < X < 200 ± 0.20
200 < X < 300 ± 0.30
ANGLES ± 0.5°

MATERIAL:

PART No.	TESI-018xx6283	DESCRIPTION	DC 2.2V(Max.),1.2~2A(Max.),18 P&N,6.2*8.3mm

SIGNATURE		DATE
DRAWN BY	Gary	2013/09/09
CHECKED BY	Justin	2013/09/09
ENGR	Vivi	2013/09/09
APPROVED BY	Mason	2013/09/09
ISSUED BY	Jack	2013/09/09

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CAD MODLE:	TESI-018xx6283.prt	SCALE: 1:1	REV: A
CAD DWG:	TESI-018xx6283.drw	SIZE: A3	SHEET: 1 OF 1