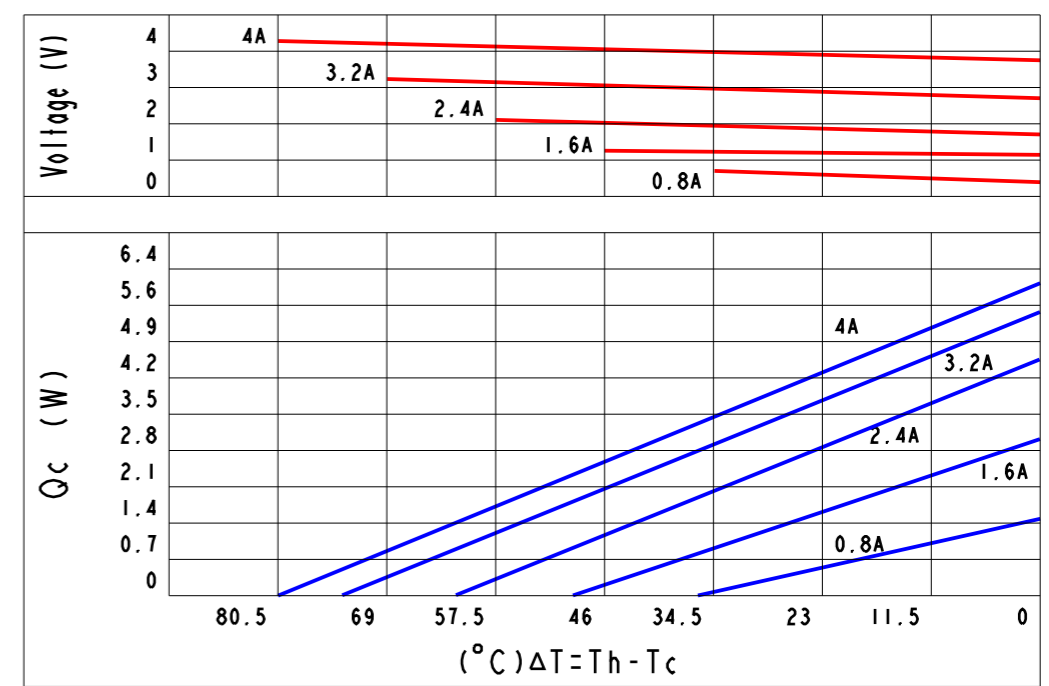


REVISIONS					
REV.	POS.	DESCRIPTION	DATE	DRW	APP
A		INITIAL CREATION	2013/09/09	Gary	Mason

Curve Chart(Be Confined To TEC2-048041520):



Part Number And Feature:

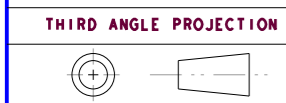
T	E	C	2	-	0	4	8	x	x	1	5	2	0	Sealing	NO
↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	Operation Temperature	125°C(Max.)
Thermo	Electric	Chip	Stage	Stack	N & P	Stack	Quantity	Current	A(Max.)	Dimension	(A)	Dimension	(B)	Melting Point	138°C
														Storage Temperature	-60°C~100°C
														RoHS	YES

Technical Data:


ITEM	Part NO.	Stack(P&N)	A(Max.)	V(Max.)	Q_c (W) / $T_h=27^{\circ}\text{C}$ / $\Delta T(^{\circ}\text{C})$	DIM(A)	DIM(B)	DIM(H)
1	TEC2-048041520	48	4 A	4 V	6W	80°C	S15	S20 RF7

- Notes:
1. Printing always on cold side.
 2. Tolerance of thermo and electric parameters ±10%.
 3. Please mount heat sink before you use it. also, please do not exceed the extra voltage at any time.
 4. Please contact with us if you need Melting Point 183°C (Operation Temperature 150°C Max.).

*DO NOT SCALE DRAWING



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USED OR DISCLOSED OR IN PART TO ANYONE WITHOUT THE PERMISSION OF KJLP (SHENZHEN) ELECTRONICS
CO., LTD.

1. UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE MM 2 TOLERANCE ARE AS FOLLOWS: 0 < X < 2 ± 0.06 2 < X < 10 ± 0.08 10 < X < 50 ± 0.12 50 < X < 100 ± 0.16 100 < X < 200 ± 0.20 200 < X < 300 ± 0.30 ANGLES ± 0.5°	PART No.	TEC2-048xx1520	DESCRIPTION	DC 4V(Max.), 4A(Max.), 48 P&N, S15*S20mm		
	SIGNATURE		DATE	 昆晶冷片(深圳)电子有限公司 KJLP (SHENZHEN) ELECTRONICS CO., LTD email: kjlp@kjlp.net http:// www.kjlp.net Tel: +86-755-82528352 Fax: +86-755-22639899		
	DRAWN BY	Gary	2013/09/09			
	CHECKED BY	Justin	2013/09/09			
ENGR	Vivi	2013/09/09				
APPROVED BY	Mason	2013/09/09	CAD MODLE:	TEC2-048xx1520.prt	SCALE: 1:1	REV: A
MATERIAL:	ISSUED BY	Jack	2013/09/09	CAD DWG:	TEC2-048xx1520.drw	SIZE: A3 SHEET: 1 OF 1