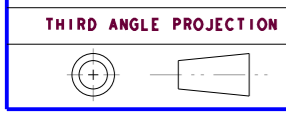


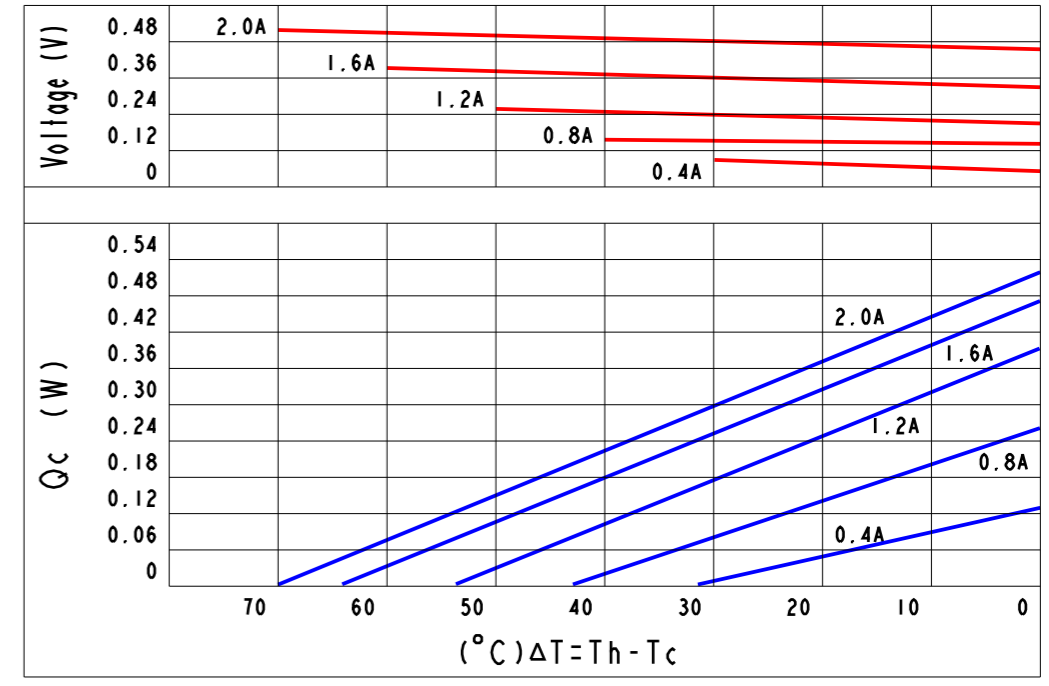
*DO NOT SCALE DRAWING



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CO., LTD.

REVISIONS					
REV.	POS.	DESCRIPTION	DATE	DRW	APP
A		INITIAL CREATION	2013/01/01	Gory	Mason

Curve Chart(Be Confined To TESI-004202263):



Part Number And Feature:


T	E	S	I	-	0	0	x	x	2	2	6	3	Sealing	NO	
↓	↓		↓	↓	↓	↓	↓	↓	↓	↓			Operation Temperature	125°C(Max.)	
Thermo	Electric	Chip(Small)	Stage	Stack	N & P	Stack	Quantity	Current	A(Max.)	Dimension	(A)	Dimension	(B)	Melting Point	138°C
														Storage Temperature	-60°C~100°C
														RoHS	YES

Technical Data:

ITEM	Part NO.	Stack(P&N)	A(Max.)	V(Max.)	Qc(W) /Th=27°C/ ΔT(°C)	DIM(A)	DIM(B)	DIM(H)
1	TESI-004122263	4	1.2 A	0.48 V	0.32W 70°C	2.2	6.3	RF2.9
2	TESI-004152263	4	1.5 A	0.48 V	0.40W 70°C	2.2	6.3	RF2.7
3	TESI-004202263	4	2 A	0.48 V	0.54W 70°C	2.2	6.3	RF2.3

Notes:

1. Printing always on cold side.
2. Torlerance of thermo and electric parameters ±10%.
3. Please mount heat sink before you use it. also, please do not exceed the extra voltage at any time.
4. Please contact with us if you need Melting Point 183°C (Operation Temperature 150°C Max.) and 235°C(Operation Temperature 200°C Max.) type.

1. UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE MM 2 TOLERANCE ARE AS FOLLOWS: 0 < X < 2 ± 0.06 2 < X < 10 ± 0.08 10 < X < 50 ± 0.12 50 < X < 100 ± 0.16 100 < X < 200 ± 0.20 200 < X < 300 ± 0.30 ANGLES ± 0.5°	PART NO.	TESI-004xx2263	DESCRIPTION	DC 0.48V(Max.), 1.2~2A(Max.), 4 P&N, 2.2*6.3mm					
	SIGNATURE		DATE	 昆晶冷片(深圳)电子有限公司 KJLP(SHENZHEN)ELECTRONICS CO., LTD email: kjlp@kjlp.net http:// www.kjlp.net Tel: +86-755-82528352 Fax: +86-755-22639899					
	DRAWN BY	Gory	2013/01/01						
	CHECKED BY	Justin	2013/01/01						
ENGR	Vivi	2013/01/01							
APPROVED BY	Mason	2013/01/01	CAD MODLE:	TESI-004xx2263.prt	SCALE:	1:1	REV:	A	
MATERIAL:	ISSUED BY	Jack	2013/01/01	CAD DWG:	TESI-004xx2263.drw	SIZE:	A3	SHEET:	1 OF 1