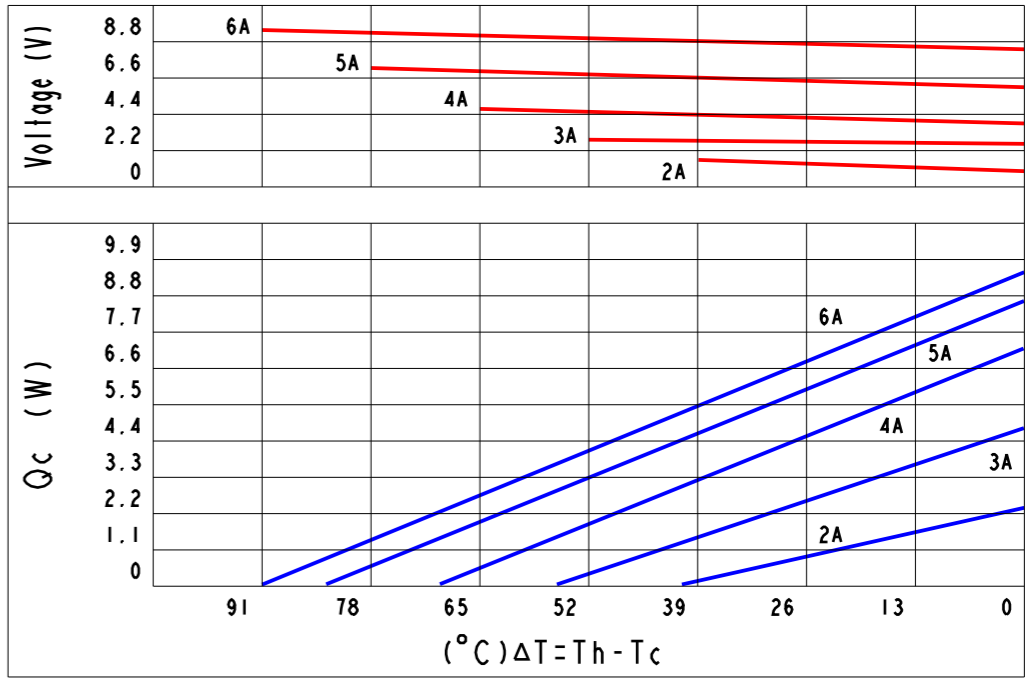


REVISIONS					
REV.	POS.	DESCRIPTION	DATE	DRW	APP
A		INITIAL CREATION	2013/01/01	Gary	Mason

Curve Chart(Be Confined To TEC3-120061530):



Part Number And Feature:

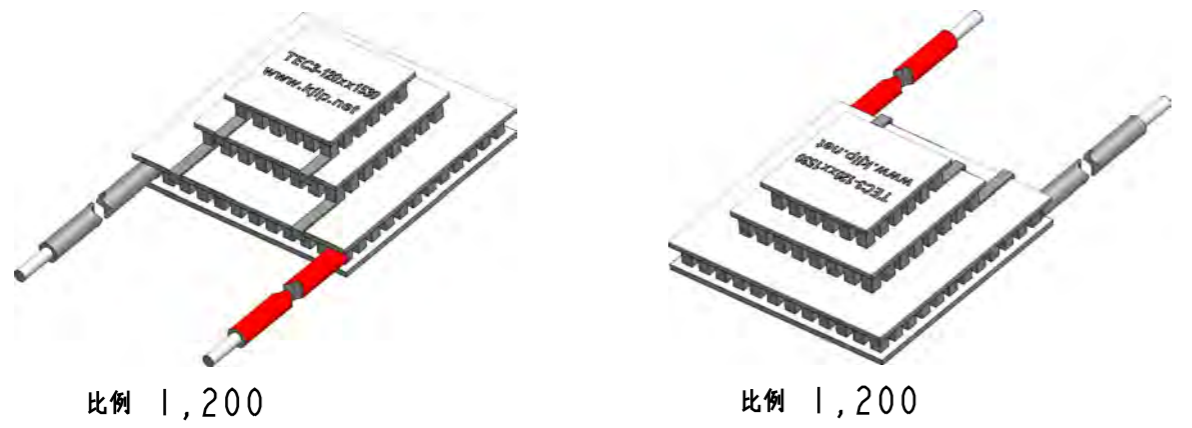
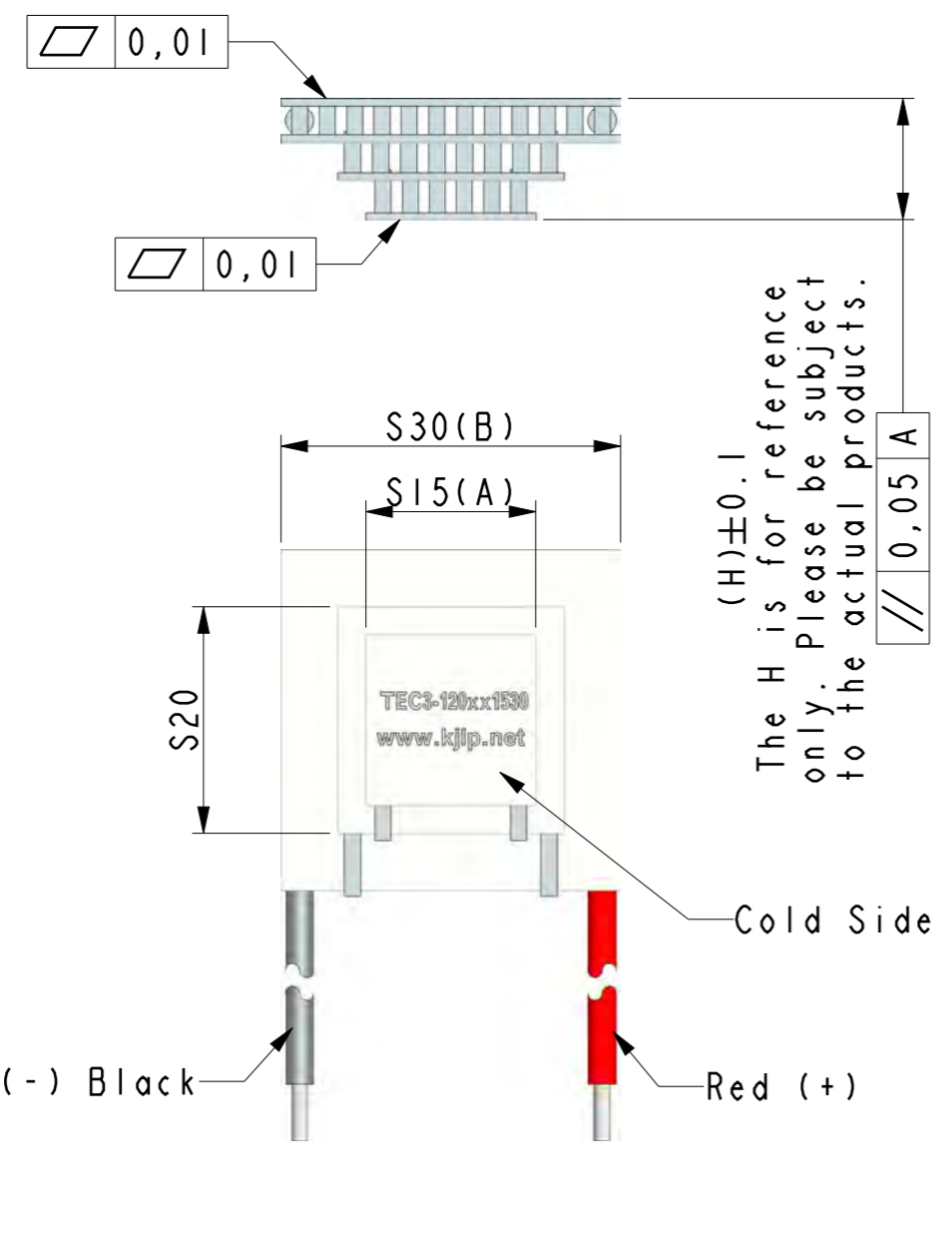
T	E	C	3	-	1	2	0	x	x	1	5	3	0	Sealing	NO
↓	↓				↓	↓	↓	↓	↓	↓	↓	↓	↓	Operation Temperature	125°C(Max.)
Thermo	Electric	Chip	Stage	Stack	N & P	Stack	Quantity	Current	A(Max.)	Dimension	(A)	Dimension	(B)	Melting Point	138°C
														Storage Temperature	-60°C~100°C
														RoHS	YES

Technical Data:

ITEM	Part NO.	Stack(P&N)	A(Max.)	V(Max.)	Qc(W) /Th=27°C/ ΔT(°C)	DIM(A)	DIM(B)	DIM(H)
1	TEC3-120021530	120	2 A	8.7 V	3.5W	90°C	S15	S30 RF10.6
2	TEC3-120041530	120	4 A	8.7 V	7W	90°C	S15	S30 RF10.4
3	TEC3-120061530	120	6 A	8.7 V	10W	90°C	S15	S30 RF9.0

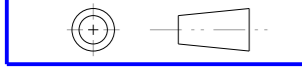
Notes:

1. Printing always on cold side.
2. Tolerance of thermo and electric parameters ±10%.
3. Please mount heat sink before you use it. also, please do not exceed the extra voltage at any time.
4. Please contact with us if you need Melting Point 183°C (Operation Temperature 150°C Max.)



*DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION



THIS DRAWING AND THE DATA DISCLOSED HEREIN OR HERewith IS NOT TO BE REPR ODUCEd USED OR DISCLOSED OR IN PART TO ANYONE WITHOUT THE PERMISSION OF KJLP (SHENZHEN) ELECTRONICS CO., LTD.

1. UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE MM
2 TOLERANCE ARE AS FOLLOWS:
0 < X < 2 ± 0.06
2 < X < 10 ± 0.08
10 < X < 50 ± 0.12
50 < X < 100 ± 0.16
100 < X < 200 ± 0.20
200 < X < 300 ± 0.30
ANGLES ± 0.5°

PART NO.	TEC3-120xx1530	DESCRIPTION	DC 8.7V(Max.), 2~6A(Max.), 120 P&N, S15*S30mm		
SIGNATURE		DATE	昆晶冷片(深圳)电子有限公司		
DRAWN BY	Gary	2013/01/01	KJLP (SHENZHEN) ELECTRONICS CO., LTD		
CHECKED BY	Justin	2013/01/01	email: kjlp@kjlp.net http:// www.kjlp.net		
ENGR	Vivi	2013/01/01	Tel: +86-755-82528352 Fax: +86-755-22639899		
APPROVED BY	Mason	2013/01/01	CAD MODLE:	TEC3-120xx1530.prt	SCALE: 1:1 REV: A
ISSUED BY	Jack	2013/01/01	CAD DWG:	TEC3-120xx1530.drw	SIZE: A3 SHEET: 1 OF 1